

COMPUTER PROCESSING SYSTEM AND PROCESSING METHOD PERFORMED IN COMPUTER

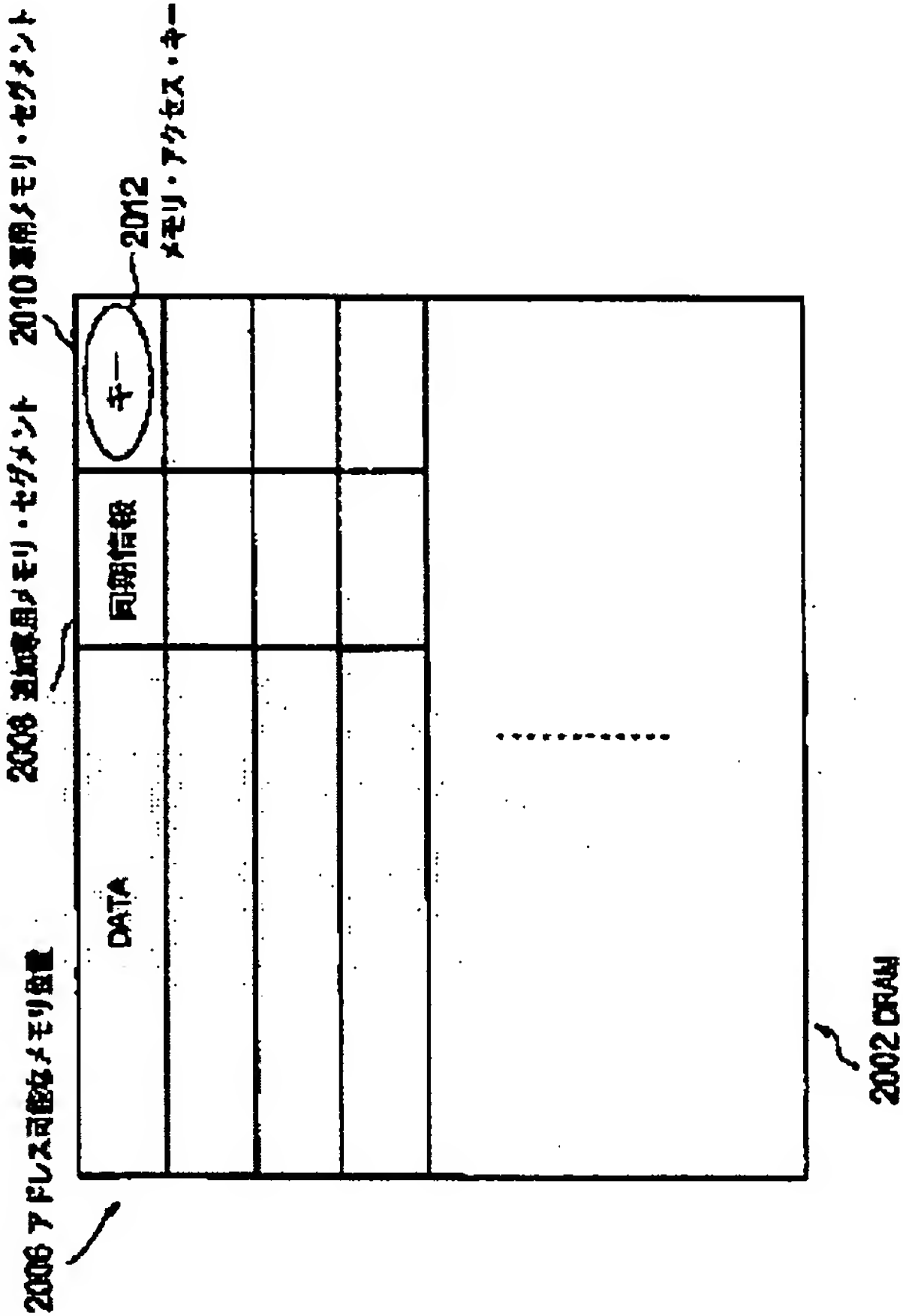
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Abstract of JP2002342165

PROBLEM TO BE SOLVED: To provide a computer architecture and programming model for high speed processing over broadband networks. SOLUTION: The architecture employs a consistent modular structure, a common computing module and uniform software cells. The common computing module includes a control processor, a plurality of processing units, a plurality of local memories from which the processing units process programs, a direct memory access, controller and a shared main memory. A synchronized system and method for the coordinated reading and writing of data to and from the shared main memory by the processing units also are provided.



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